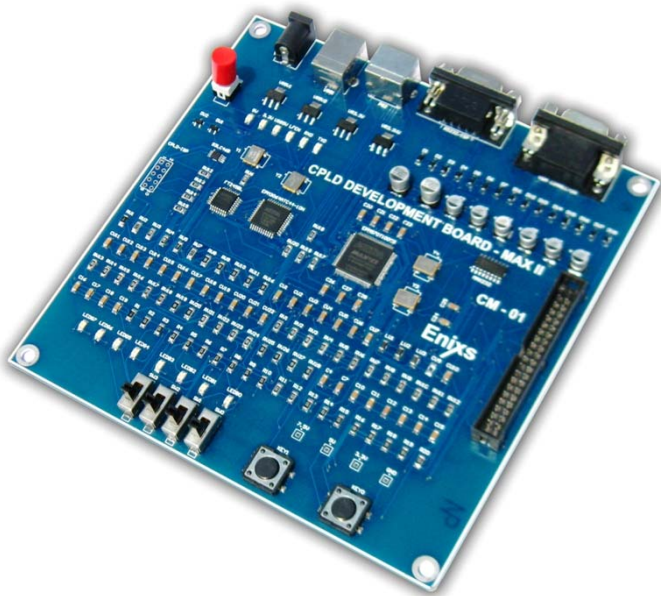


## Introduction

The CPLD development board uses the MAX II family Altera CPLD chip. This low cost CPLD chip is very appropriate for entry level use .It is used to expand your own CPLD projects with external circuits by all IO ports such as LEDS ,Serial communication,PS2 and VGA interfacing .It has inbuilt USB blaster. It provides a convenient hardware platform for the user to learn and develop own CPLD projects. It provides the general purpose, low-density logic applications.



## Board Specification

- Altera Max II family EPM570T100C5 is used
- Power provision:7.5V power adaptor
- Supportable to USB programming
- Power ON/OFF switch
- In built USB blaster made by EPM3064ATC44-10N
- Provision for PS2 keyboard interfacing
- Provision for VGA interfacing
- RS232 port for serial communication
- 40 Pin GPIO Connector
- On board IO devices
  - Eight user defined LEDs
  - Two user defined Push buttons
  - Four user defined Slide switches

## Chip Specification

- Altera Max II -EPM570T100C5
- Low cost low power CPLD
- Operating voltage 2.3V to 3.6V
- 440 macro cells
- User IO pins 76

## Compatible Interfaces

- ADC 0809 Interface card.
- DAC 0800 Interface card.
- DIO-IV Interface card.
- Elevator Interface card.
- I2C interface card.
- Model Train controller Interface card.
- Motor Interface card.
- Sensor Interface card.
- Traffic light Controller interface card.
- Wireless Interface Card.

